

E-FILED ON 08 December 2006

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of inventor(s):
Albert Sun et al.

Application No. **10/699,764**

Filing Date: **03 November 2003**

Title: **In-Circuit Configuration
Architecture for Embedded
Configurable Logic Array**

Confirmation No. **4234**

Group Art Unit: 2186

Examiner: Hetul B. Patel

CUSTOMER NO. 22470

MAIL STOP AMENDMENT

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

RESPONSE

Sir:

In response to the non-final Office Action mailed 24 August 2006, Applicants request the following in the above-referenced application:

Remarks begin on page 2 of this paper.

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